

ATTORNEY DOCKET NO.
1067-CA-C1 (P108C1)

PATENT
PARENT Serial No. ~~09/651,821~~

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: John Laurence Melanson

PARENT Serial No.: ~~09/651,821~~

Filed:

10/7/5-950

Art Unit: Not yet assigned

Examiner: Not yet assigned

For: CIRCUITS AND METHODS FOR REDUCING INTERFERENCE FROM SWITCHED
MODE CIRCUITS

PRELIMINARY AMENDMENT

Mail Stop: New Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to examination on the merits, please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper. Attached to this document is a substitute specification for the Examiners convenience.

Amendments to the Claims are reflected in the listing of claims which begins on page 4

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01 FC:1202 of this paper.

02 FC:1201 180.00 DA

177.00 DA
Remarks begins on page 9 of this paper.

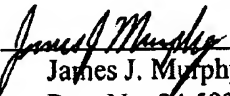
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first and second clock signals and consequently the operating characteristics of a pulse width modulation circuit.

Applicant believes that no further fees are due. However, the Director is Authorized to debit any amounts due by this paper to Deposit Account No. 23-2426 of Winstead Sechrest & Minick P.C.

Applicant respectfully requests that Applicant's attorney be called at the below listed number if it is believed that such a discussion would be helpful in resolving any problems or questions.

Respectfully submitted,
WINSTEAD SECHREST & MINICK P.C.
Attorneys for Applicant

By: 
James J. Murphy
Reg. No. 34,503

Date: November 17, 2003

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